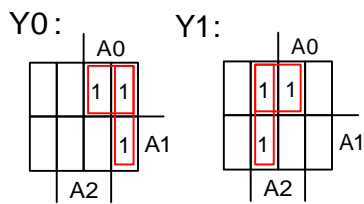


Programmiertabelle

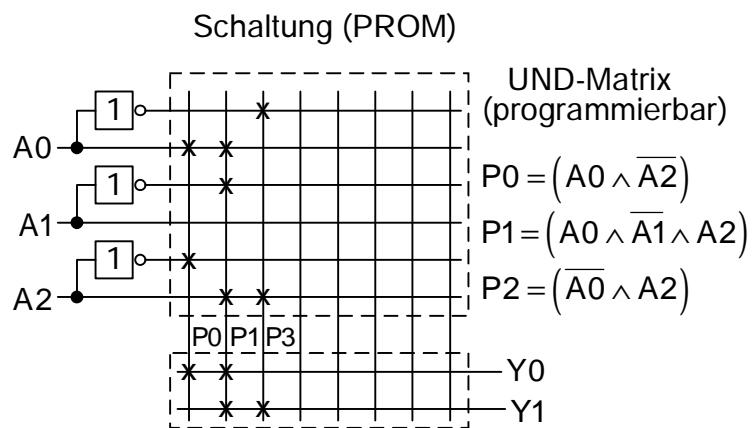
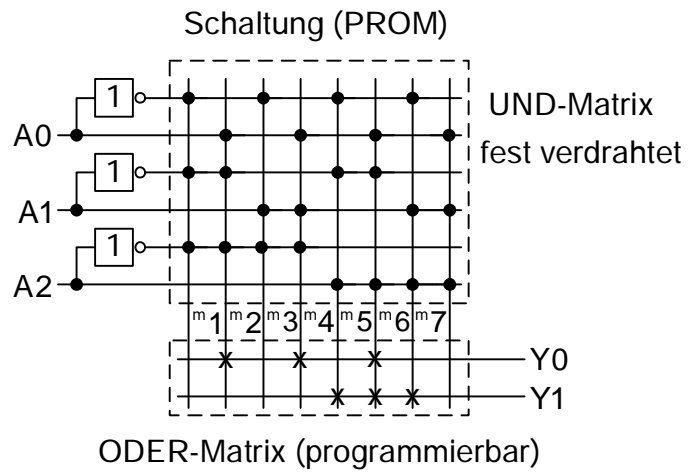
A2	A1	A0	Y1	Y0
0	0	0	0	0
0	0	1	0	1
0	1	0	0	0
0	1	1	0	1
1	0	0	1	0
1	0	1	1	1
1	1	0	1	0
1	1	1	0	0

Minimierung  
PROM Schaltnetz



$$Y0 = (A0 \wedge \overline{A2}) \vee (A0 \wedge \overline{A1} \wedge A2)$$

$$Y1 = (\overline{A0} \wedge A2) \vee (A0 \wedge \overline{A1} \wedge A2)$$



Schaltentwurf bei Einsatz eines PLA ODER-Matrix (programmierbar)

Programmiertabelle

A2	A1	A0	Y1	Y0
0	0	0	0	0
0	0	1	0	1
0	1	0	0	0
0	1	1	0	1
1	0	0	1	0
1	0	1	1	1
1	1	0	1	0
1	1	1	0	0

$$Y0 = (A0 \wedge \overline{A2}) \wedge (A0 \wedge \overline{A1} \wedge A2)$$

$$Y1 = (\overline{A0} \wedge A2) \vee (A0 \wedge \overline{A1} \wedge A2)$$

Schaltentwurf bei Einsatz eines PAL

